



**LOYOLA COLLEGE (AUTONOMOUS), CHENNAI – 600 034**

**B.Sc.DEGREE EXAMINATION –COMPUTER SCIENCE**

**THIRD SEMESTER – APRIL 2019**

**PH 3106– APPLIED ELECTRONICS**

Date: 13-04-2019  
Time: 01:00-04:00

Dept. No.

Max. : 100 Marks

**PART - A**

**Answer any FOUR questions**

**(4×10=40)**

1. Differentiate RAM and ROM.
2. Define CMRR.
3. What is a semiconductor? Name the types
4. Draw the logic symbol and truth table of D-flip flop.
5. Draw and explain the half adder and write the truth table.
6. State and prove de Morgan's theorem.
7. Draw the diagram of JK Flip flop and give its truth table.
8. Write a note on solar cell.

**PART - B**

**Answer any THREE questions**

**(3×20=60)**

9. Explain the various components in memory hierarchy with a block diagram.
10. Draw the diagram of full adder and give the truth table.
11. Explain the working of 4-1 multiplexer with neat diagram.
12. Draw and explain the working of an inverting amplifier.
13. Write a note on computer registers.
14. Show NAND as a universal gate.

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